

PATENT APPLICATION

Docket No.: 4591-306

Serial No. not yet assigned

Applicant: Jae-Yoon Sim

Filing Date: June 30, 2003

Group: not yet assigned

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	Document <u>Number</u>	Issue <u>Date</u>	<u>Name</u>	<u>Class</u>	Sub <u>Class</u>
_____	_____					

FOREIGN PATENT DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	Document <u>Number</u>	Publication <u>Date</u>	<u>Country</u>	<u>Name</u>
_____	_____				

OTHER DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
---------------------	------------	--

Article from IEEE Journal of Solid-State Circuits, Vol. 29 No. 1, pp. 9-13
January 1994, entitled "OFFSET COMPENSATING BIT-LINE SENSING
SCHEME FOR HIGH DENSITY DRAM'S"

Examiner: _____

Date Considered: _____